Specifications

18200-20

Cole-Parmer Instrument Company
625 East Bunker Court
Vernon Hills, Illinois 60061-1844
(847) 549-7600
(847) 247-2929 (Fax)
800-323-4340
www.coleparmer.com
e-mail: techinfo@coleparmer.com
Document Revision 1.0, August, 2008

Specifications

Typical for 25 °C unless otherwise specified.

Specifications in *italic text* are guaranteed by design.

Analog input section

Table 1. Analog input specifications

Parameter	Conditions	Specification
A/D converter type		16-bit successive Approximation type
Number of channels		8 single-ended
Input configuration		Individual A/D per channel
Sampling method		Simultaneous
Absolute maximum input voltage	*CHx IN to GND.*	*±15 V max*
Input impedance		100 MOhm, min
Input ranges	Software selectable	±10 V, ±5 V, ±2 V, ±1 V
Sampling rate	Scan to PC memory	0.6 S/s to 50 kS/s, software programmable
	Burst scan to 32 k sample FIFO	20 S/s to 50 kS/s, software programmable
Throughput	Software paced	500 S/s all channels
	Scan to PC memory (Note 1)	= (100 kS/s) / (# of channels), max of 50 kS/s for any channel
	Burst scan to 32 k sample FIFO	= (200 kS/s) / (# of channels), max of 50 kS/s for any channel
Gain queue		Software configurable. Eight elements, one gain element per channel.
Resolution		16 bits
No missing codes		*15 bits*
Crosstalk	Signal DC-25 KHz	-80 dB
CAL output	User calibration source	0.625 V, 1.25 V, 2.5 V, 5.0 V, software selectable
CAL output accuracy (Note 2)		±0.5% typ, ±1.0% max
CAL current		±5 mA max
Trigger source	Software selectable	External digital: TRIG_IN

Note 1: Maximum throughput scanning to PC memory is machine dependent. While the majority of XP equipped PC's we tested allowed acquisition at the maximum rates, a few would not. The lowest maximum rate we observed on an XP equipped PC during multi-channel testing was 95 kS/s, aggregate. The rates specified are for Windows XP only. Maximum rates on operating systems that predate XP may be less and must be determined through testing on your machine.

Note 2: Actual values used for calibration are measured and stored in EEPROM.

Table 2. Calibrated absolute accuracy

Range	Accuracy (mV)
±10 V	5.66
±5 V	2.98
±2 V	1.31
±1 V	0.68

Table 3. Accuracy components - All values are (±)

Range	% of Reading	Gain error at FS (mV)	Offset (mV)
±10 V	0.04	4.00	1.66
±5 V	0.04	2.00	0.98
±2 V	0.04	0.80	0.51
±1 V	0.04	0.40	0.28

Table 4 summarizes the noise performance for the 18200-20. Noise distribution is determined by gathering 50 K samples with inputs tied to ground at the user connector. Samples are gathered at the maximum specified sampling rate of 50 kS/s.

Table 4. Noise performance

Range	Typical counts	LSBrms
±10 V	10	1.52
±5 V	10	1.52
±2 V	11	1.67
±1 V	14	2.12

Digital input/output

Table 5. Digital I/O specifications

Digital type	CMOS
Number of I/O	8 (DIO0 through DIO7)
Configuration	Independently configured for input or output
Pull-up/pull-down configuration	All pins pulled up to Vs via 47 K resistors (default). Positions available for pull down to ground. Hardware selectable via zero ohm resistors as a factory option.
Input high voltage	2.0 V min, 5.5 V absolute max
Input low voltage	0.8 V max, –0.5 V absolute min
Output high voltage (IOH = –2.5 mA)	3.8 V min
Output low voltage (IOL = 2.5 mA)	0.7 V max
Power on and reset state	Input

External trigger

Table 6. External trigger specifications

Parameter	Conditions	Specification
Trigger source (Note 3)	External digital	TRIG_IN
Trigger mode	Software selectable	Edge sensitive: user configurable for CMOS compatible rising or falling edge.
Trigger latency		10 µs max
Trigger pulse width		1µs min
Input high voltage		4.0 V min, 5.5 V absolute max
Input low voltage		1.0 V max, –0.5 V absolute min
Input leakage current		*±1.0µA*

Note 3: TRIG_IN is a Schmitt trigger input protected with a 1.5K Ohm series resistor.

External clock input/output

Table 7. External clock I/O specifications

Parameter	Conditions	Specification
Pin name		SYNC
Pin type		Bidirectional
Software selectable direction	Output	Outputs internal A/D pacer clock.
	Input	Receives A/D pacer clock from external source.
Input clock rate		50 kHz, maximum
Clock pulse width	Input	1µs min
	Output	5µs min
Input leakage current		*±1.0µA*
Input high voltage		4.0 V min, 5.5 V absolute max
Input low voltage		1.0 V max, –0.5 V absolute min
Output high voltage (Note 4)	IOH = –2.5 mA	3.3 V min
	No load	3.8 V min
Output low voltage (Note 4)	IOL = 2.5 mA	1.1 V max
	No Load	0.6 V max

Note 4: SYNC is a Schmitt trigger input and is over-current protected with a 200 Ohm series resistor.

Counter section

Table 8. Counter specifications

Pin name (Note 5)	CTR
Counter type	Event counter
Number of channels	1
Input type	TTL, rising edge triggered
Input source	CTR screw terminal
Resolution	32 bits
Schmidt trigger hysteresis	*20 mV to 100 mV*
Input leakage current	*±1µA*
Maximum input frequency	1 MHz
High pulse width	*500 ns min*
Low pulse width	*500 ns min*
Input high voltage	4.0 V min, 5.5 V absolute max
Input low voltage	1.0 V max, –0.5 V absolute min

Note 5: CTR is a Schmitt trigger input protected with a 1.5K Ohm series resistor.

Memory

Table 9. Memory specifications

Data FIFO	32,768 samples, 65,536 bytes		
EEPROM	1,024 bytes		
EEPROM configuration	**Address range**	**Access**	**Description**
	0x000-0x07F	Reserved	128 bytes system data
	0x080-0x1FF	Read/write	384 bytes cal data
	0x200-0x3FF	Read/write	512 bytes user area

Microcontroller

Table 10. Microcontroller specifications

Type	*High performance 8-bit RISC microcontroller*
Program memory	*16,384 words*
Data memory	*2,048 bytes*

Power

Parameter	Conditions	Specification
Supply current	USB enumeration	< 100 mA
Supply current (Note 6)	Continuous mode	150 mA
+5 V USB power available (Note 7)	▪ Connected to self-powered hub ▪ Connected to externally-powered root port hub	4.5 V min, 5.25 V max
Output current (Note 8)		350 mA max

Note 6: This is the total current requirement for the 18200-20 which includes up to 10 mA for the status LED.

Note 7: "Self-powered hub" refers to a USB hub with an external power supply. Self-powered hubs allow a connected USB device to draw up to 500 mA. "Root port hubs" reside in the PC's USB host Controller. The USB port(s) on your PC are root port hubs. All externally-powered root port hubs (desktop PC's) provide up to 500 mA of current for a USB device. Battery-powered root port hubs provide 100 mA or 500 mA, depending upon the manufacturer. A laptop PC that is not connected to an external power adapter is an example of a battery-powered root port hub. If your laptop PC is constrained to the 100 mA maximum, you need to purchase a self-powered hub.

Note 8: This refers to the total amount of current that can be sourced from the USB +5 V and digital outputs.

General

Device type	USB 2.0 (full-speed)
Device compatibility	USB 1.1, USB 2.0

Environmental

Operating temperature range	0 to 70 °C
Storage temperature range	-40 to 70 °C
Humidity	0 to 90% non-condensing

Mechanical

Dimensions	79 mm (L) x 82 mm (W) x 25 mm (H)
USB cable length	*3 meters max*
User connection length	*3 meters max*

Main connector and pin out

Connector type	Screw terminal
Wire gauge range	16 AWG to 30 AWG

Pin	Signal Name	Pin	Signal Name
1	CH0 IN	21	DIO0
2	AGND	22	GND
3	CH1 IN	23	DIO1
4	AGND	24	GND
5	CH2 IN	25	DIO2
6	AGND	26	GND
7	CH3 IN	27	DIO3
8	AGND	28	GND
9	CH4 IN	29	DIO4
10	AGND	30	GND
11	CH5 IN	31	DIO5
12	AGND	32	GND
13	CH6 IN	33	DIO6
14	AGND	34	GND
15	CH7 IN	35	DIO7
16	AGND	36	SYNC
17	CAL	37	TRIG_IN
18	AGND	38	CTR
19	AGND	39	PC +5V
20	AGND	40	GND

Cole-Parmer Instrument Company
625 East Bunker Court
Vernon Hills, Illinois 60061-1844
(847) 549-7600
Fax: (847) 247-2929
800-323-4340
www.coleparmer.com
E-mail: techinfo@coleparmer.com